# Press Release

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## Teranetics Announces Industry's Smallest Quad 10GBASE-T PHY

### New 40nm Family Expands Industry's Leading 10GBASE-T PHY Product Line, Enables Quad, Dual, and Single-Port Designs with Low Power

**SAN JOSE, Calif., April 19, 2010—**Teranetics, the leading provider of 10GBASE-T physical layer solutions, today announced its new 40nm TN8000 Family of PHYs. Among this third generation of PHYs, the TN8044 is the industry's smallest quad-port 10GBASE-T PHY further driving dense, highly-efficient, switch applications. The TN8022 (dual-port) and TN8020 (single-port) offer the power-efficiency required to enable 10GBASE-T adapter card designs.

Housed in a 27x27mm BGA package, the TN8044 is the industry's smallest quad-10GBase-T PHY. The new TN8000 family, continuing Teranetics low-power leadership, dissipates less than 4 watts per port at a full 100 meters, and as little as 2 watts per port in short reach mode. In addition, this third-generation, 40nm family continues Teranetics' long history of supporting triple rate Ethernet (100M/1G/10G), while also integrating EEE support enabling even greener designs and MACSec capability permitting secure LAN data transmission.

"The TN8000 family shows that Teranetics continues to push the envelope for 10-Gigabit Ethernet over copper with our third-generation PHY," said **Nersi Nazari**, president and CEO of **Teranetics**. "With our unrivaled experience in supplying real-world products in high volumes over the past two years, Teranetics is well positioned to continue its industry leadership in the 40-nanometer era."

The TN8000 family will also offer support for the emerging Energy Efficient Ethernet standard (EEE, or IEEE 802.3az). Teranetics CTO and Chairman Sanjay Kasturia is the Editor in chief for the IEEE 802.3az committee developing the EEE standard. The ability to reduce the power consumption of Ethernet

transceivers (PHYs) when the actual data rate required is less than the peak available rate, along with operation over existing infrastructure makes the TN8000 family of PHYs the best option for energy-efficient system design.

Teranetics has led the 10GBASE-T PHY market since it first began shipping in 2008, and the TN8000 family shows that the company is intent on maintaining its lead." said **Jag Bolaria**, senior analyst at **The Linley Group**. "We believe that low-power 40nm designs like this will lead to broader adoption of 10-Gigabit Ethernet over copper in the data center."

With its small form factor and low power dissipation, the TN8040 PHYs are ideal for data center switch and server vendors who want to offer dense arrays of high-speed Ethernet connections in their products. High Performance Computing in the Financial, Oil&Gas, and Cloud Computing verticals are demanding 10 Gigabit Ethernet densities to service volume bandwidth requirements. Teranetics was the first to enable 10GBASE-T dense switching designs by shipping the first dual-port PHY and has shipped far more dual-port 10GBASE-T PHYs than any other vendor. This new product supports even denser equipment by supporting quad-port designs with high performance and low power.

Teranetics has used its technology leadership to further reduce the external component count required to build systems enabling further reductions in the cost of 10GBASE-T ports. The TN8000 family allows multiple PHYs to share reference clocks and power regulators, and also integrates an EMI filter that previously was built with external components on the PC board. Moreover, MDI test capabilities reduce manufacturing costs by increasing equipment manufacturers' final yield and decreasing test time.

Teranetics' multi-generation portfolio of 10GBASE-T PHY increases scalability and improves throughput at a dramatically reduced cost, making the ownership of 10-Gigabit Ethernet links possible for more data centers and enterprise networks. Teranetics' unique single-chip PHY solution has made it the top choice among numerous OEMs and equipment manufacturers.

**Teranetics Hosts 10 Gigabit Ethernet Reception & Speaker Panel at Interop**
Teranetics will host a 10-Gigabit Ethernet cocktail reception on Tuesday, April 27, from 5:30 to 9:00 PM in the Palm Room and Lounge at the Four Seasons Hotel. The reception will feature a panel of speakers within the 10 Gigabit market ranging from Switches, NICs, Industry Analysts, and Cable Vendors, including Sanjay Kasturia of Teranetics; Jag Bolaria of the Linley Group; Rupesh Chakkingal of Cisco, David Chalupsky of Intel, Valerie Maguire of Siemon, and John Monson of Mellanox. For reception details, please contact http://www.teranetics.com/interop.html.

**About Teranetics**

Teranetics is a leading provider of silicon solutions that enable significantly higher data rates over structured copper cabling than are currently available in today's Ethernet network environments. Led by a team with exceptional expertise and experience in the development and application of mixed-signal semiconductor solutions and digital communication technologies, Teranetics is the first company to ship a production-ready 10GBASE-T semiconductor designed for next generation network applications. Teranetics is backed by a strong consortium of venture investors with in-depth experience and success in the semiconductor and data networking markets. For more information, go to www.teranetics.com.

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